

Editorial

THIS second issue of 2015 collects some papers in the area of communications, information technology, power systems, aerospace, cyberphysical systems, and system engineering. Papers address various topics encompassing theory, technologies, design methodologies, and applications.

In order to increase the effectiveness in serving our community, the IEEE Systems Council (which is sponsoring our journal) approved a new policy concerning the page limits, i.e., up to 8 pages are complimentary (including text, figures, and tables, as well as optional authors' biographies and photographs) and up to additional 4 pages are allowed with the extra page charge of \$150 per page. This new policy will apply to all papers submitted from January 1, 2015, including tutorial and survey papers.

If you have any comments, suggestions, recommendations, or criticisms about this journal, please do not hesitate to contact me. Everyone can help in continuously improving our journal. We aim to continue to have a live publication for our live community.

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Vincenzo Piuri (S'84–M'86–SM'96–F'01) received the Ph.D. degree in computer engineering from Politecnico di Milano, Milan, Italy, in 1989.

He was an Associate Professor with Politecnico di Milano and a Visiting Professor with the University of Texas at Austin, Austin, TX, USA, and George Mason University, Fairfax, VA, USA. Since 2000, he has been a Full Professor in computer engineering with the Department of Computer Science, Università degli Studi di Milano, Milan, Italy, where also served as the Director of the Department of Information Technology in 2007–2012. Since 2014, he has been an Honorary Professor with Obuda University, Budapest, Hungary, and a Guest Professor (equivalent to an Honorary Professor) with Guangdong University of Petrochemical Technology, Maoming, China. He is the author or coauthor of more than 350 papers in international journals, proceedings of international conferences, books, and book chapters. His current research interests include theory and industrial applications of neural networks, machine learning, signal and image processing, pattern analysis and recognition, biometrics, intelligent measurement systems, industrial applications, fault tolerance, digital processing architectures, embedded systems, and arithmetic architectures.

Dr. Piuri is a Distinguished Scientist of the Association for Computing Machinery and a senior member of the International Neural Network Society. He is the Editor-in-Chief of the *IEEE Systems Journal* in 2013–2015 and was an Associate Editor of the *IEEE Computational Intelligence Magazine* in 2010–2012, the IEEE TRANSACTIONS ON NEURAL NETWORKS in 2001–2004, the IEEE TRANSACTIONS ON INSTRUMENTATION AND MEASUREMENT in 1997–2002, and various other international journals. He is the 2015 IEEE Vice President for Technical Activities, and he was the IEEE Director and an IEEE Delegate for Division X in 2010–2012, the President of the IEEE Computational Intelligence Society in 2006–2007, the Vice President for Publications of the IEEE Instrumentation and Measurement Society in 2002–2004 and of the IEEE Systems Council in 2008–2011, the Vice President for Membership of the IEEE Computational Intelligence Society in 2003–2004, and the Vice President for Education of the IEEE Biometrics Council in 2008–2009. He was the recipient of the IEEE Instrumentation and Measurement Society Technical Award in 2002 for his contributions to the advancement of theory and practice of computational intelligence in measurement systems and industrial applications, the IEEE Instrumentation and Measurement Society Distinguished Service Award in 2008, and the IEEE Computational Intelligence Society Meritorious Service Award in 2009.